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Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT MAR 28 2005 (use as many sheets as necessary)				Complete If Known	
				Application Number	10/829,575
				Filing Date	April 22, 2004
				First Named Inventor	Richard Brown, et al.
				Group Art Unit	2817
				Examiner Name	
Sheet 1 of 2				Attorney Docket Number	UOM 0295 PUSP

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

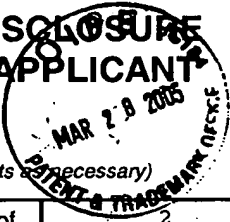
[illegible]

Examiner Signature	T. Cunningham	Date Considered	2/06/08
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

* Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Substitute for Form 1449B/PTO			Complete if Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (use as many sheets as necessary)			Application Number	10/829,575	
			Filing Date	April 22, 2004	
			First Named Inventor	Richard Brown, et al.	
			Group Art Unit	2817	
			Examiner Name		
Sheet	2	of	2	Attorney Docket Number	UOM 0295 PUSP
OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
TDC		MCCORQUODALE, MICHAEL S., ET AL., A CMOS Voltage-to-Frequency Linearizing Preprocessor for Parallel Plate RF MEMS Varactors, IEEE MTT-S Digest, June, 2003, pp. A21-A24.			

Examiner Signature	T. Cunningham	Date Considered	2/15/06
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Substitute for Form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	10/829,575
Filing Date	April 22, 2004
First Named Inventor	Michael S. McCorquodale, et
Group Art Unit	2817
Examiner Name	
Attorney Docket Number	UOM 0295 PUSP

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TDC	—	REBEIZ, GABRIEL M., ET AL., RF MEMS Switches and Switch Circuits, IEEE Microwave Magazine, Vol. 2, Issue 4, December 2001, pp. 59-71	
TDC	—	NGUYEN, C.-T., High-Q Micromechanical Oscillators and Filters for Communications, IEEE International Symposium on Circuits and Systems, Hong Kong, June 9-12, pp. 2825-2828	
TDC	—	YOUNG, DARRIN J., ET AL., A Micromachined-Based RF Low-Noise Voltage-Controlled Oscillator, IEEE Custom Integrated Circuits Conference, 1997, pp. 431-434	
TDC	—	YOUNG, DARRIN J., ET AL., Monolithic High-Performance Three-Dimensional Coil Inductors for Wireless Communication Applications, International Electron Devices Meeting, 1997, pp. 3.5.1 - 3.5.4	
TDC	—	ZOU, JUN, ET AL., Development of a Wide Tuning Range MEMS Tunable Capacitor for Wireless Communication Systems, International Electron Devices Meeting, 2000, pp. 403-406	
TDC	—	VITTOZ, ERIC, ET AL., CMOS Analog Integrated Circuits Based on Weak Inversion Operation, IEEE Journal of Solid-State Circuits, Vol. SC-12, No. 3, June 1977, pp. 224-231	
TDC	—	FILANOVSKY I.M., ET AL., Simple CMOS Analog Square-Rooting and Squaring Circuits, IEEE Trans on Circuits and Systems I: Fundamental Theory and Applications, Vol. 39, No. 4, April 1992	
TDC	—	GREGORIAN R., ET AL., Analog MOS Integrated Circuits for Signal Processing, New York: John Wiley & Sons, 1986, pp. 450	

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